

Free/Open Source Hardware, What it means to Design Engineers

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Fedora Electronic Lab

fedora
ELECTRONIC LAB

The Fedora Project is out front for you, leading the advancement of free, open software and content.

fedora ^f

Community Leader in opensource EDA deployment.

Empowers you with an advanced hardware design and simulation platform for micro-nano electronics engineering.

Design/Model



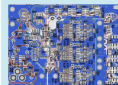
Simulate



ASICs



Evaluation Boards



<http://chitlesh.fedorapeople.org/FEL>

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EDA-FEL-012/PP#02.00

The Fedora Project is maintained and driven by the community and sponsored by Red Hat. This is a community maintained document. Red Hat is not responsible for content.

Sources

You will find many resources and tricks on the Internet, but solutions to all technical issues lie only in the Source.



Thanks to LucasArts



Source: <http://xkcd.com/191/>

Examples

- * LatticeMico 32 open source license agreement
- * Nangate Open Cell library license
- * TAPR Open Hardware license
- * Trusster.com: Teal, Truss

End Software Patents



Open Invention Network (OIN)

The Interested

The Ignorant

The Innocent

References

- * Yoda slide. Kernel Advice and Resources. CC BY SA 3.0.
<http://free-electrons.com/doc/kernel-advice-and-resources.odp>
- * Lojban comic. xkcd.com. CC Attribution NonCommercial 2.5.
<http://xkcd.com/191/>
- * End Software Patents. <http://endsoftpatents.org/>
- * Fedora Electronic Lab. <http://spins.fedoraproject.org/fel/>